

<b>L Numb r</b>	<b>Hits</b>	<b>S arch T xt</b>	<b>DB</b>	<b>Tim stamp</b>
96	15	333/\$.CCLS. AND TRANSF RMER AND FILTER AND POLARIZER AND WAVEGUIDE	USPAT; US-P PUB; EP ; JP ; DERWENT; IBM_TDB	2003/04/02 14:01
97	421	333/104, 262.CCLS. AND IMPEDANCE AND SWITCH AND ( FET FIELD ADJ EFFECT ADJ TRANSISTOR)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/02 14:02
98	210	(333/104, 262.CCLS. AND IMPEDANCE AND SWITCH AND ( FET FIELD ADJ EFFECT ADJ TRANSISTOR) ) AND SWITCH\$4.TI.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/02 14:16
99	32	((333/104, 262.CCLS. AND IMPEDANCE AND SWITCH AND ( FET FIELD ADJ EFFECT ADJ TRANSISTOR) ) AND SWITCH\$4.TI.) AND (IMPEDANCE AND (FET TRANSISTOR)).CLM.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/02 14:05
100	79	((333/104, 262.CCLS. AND IMPEDANCE AND SWITCH AND ( FET FIELD ADJ EFFECT ADJ TRANSISTOR) ) AND SWITCH\$4.TI.) AND (IMPEDANCE ADJ3 ( NETWORK CIRCUIT MATCH\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/02 14:13
104	13	((FET TRANSISTOR) AND SWITCH\$4).TI. AND 333/\$.CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/02 14:11
115	1107	333/\$.CCLS. AND SWITCH\$4.TI.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/02 14:17
116	98	(333/\$.CCLS. AND SWITCH\$4.TI.) AND ( ( FET TRANSISTOR) AND IMPEDANCE ADJ3 ( NETWORK CIRCUIT TRANSFORM\$4 MATCH\$4)).CLM,	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/02 14:18
117	17	(333/\$.CCLS. AND SWITCH\$4.TI.) AND ( ( FET TRANSISTOR) AND IMPEDANCE ADJ3 ( NETWORK CIRCUIT TRANSFORM\$4 MATCH\$4)).CLM.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/02 14:28